

Amendments to the Specification

On page 1 of the subject application, please amend paragraph 27 of the specification as follows:

FIG. 1 shows a schematic of a processing platform 10 according to an embodiment of the present invention. An I/O processor 14 provides a first processing system for communicating with a peripheral device 16 and a host processing system 12 provides a second processing system for communicating with the peripheral device 16. The I/O processor 14 comprises an internal bridge 19 which forms a primary bus 24 and a secondary bus 18. According to an embodiment, the primary and secondary busses 24 and 18 may be formed according to a PCI data bus structure such as that described in the PCI Local Bus Specification, Rev. 2.2, Dec. 18, 1998 published by the PCI Special Interest Group (hereinafter the "PCI Local Bus Specification"). However, this is merely an example of a bus structure which may be employed in a data bus to transmit data between devices and embodiments of the present invention are not limited in this respect. Also, the internal bridge may be formed according to the PCI-to-PCI Bridge Specification. However, this is merely an example of how a bridge may be implemented to form primary and secondary data busses in a processing platform and embodiments of the present invention are not limited in this respect.